Cisco CISCO2951/K9 (revision 1.0) with 483328K/40960K bytes of memory.
Processor board ID FTX1351AHSK
3 Gigabit Ethernet interfaces
4 Voice FXO interfaces
4 Voice FXS interfaces
DRAM configuration is 72 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
254464K bytes of ATA System CompactFlash 0 (Read/Write)

Cisco CISCO2921/K9 (revision 1.0) with 471040K/53248K bytes of memory.
Processor board ID FTX1428AJJF
3 Gigabit Ethernet interfaces
1 terminal line
2 Channelized (E1 or T1)/PRI ports
1 Virtual Private Network (VPN) Module
4 Voice FXO interfaces
4 Voice FXS interfaces
1 Services Module (SM) with Services Ready Engine (SRE)
DRAM configuration is 64 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
254464K bytes of ATA System CompactFlash 0 (Read/Write)

Cisco CISCO2921/K9 (revision 1.0) with 483328K/40960K bytes of memory.
Processor board ID FTX1710ALJP
3 Gigabit Ethernet interfaces
1 terminal line
2 Voice FXO interfaces
4 Voice FXS interfaces
DRAM configuration is 64 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
250880K bytes of ATA System CompactFlash 0 (Read/Write)

Cisco CISCO2921/K9 (revision 1.0) with 483328K/40960K bytes of memory.
Processor board ID FTX1710ALJZ
3 Gigabit Ethernet interfaces
1 terminal line
2 Voice FXO interfaces
4 Voice FXS interfaces
DRAM configuration is 64 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
250880K bytes of ATA System CompactFlash 0 (Read/Write)

Cisco CISCO2921/K9 (revision 1.0) with 483328K/40960K bytes of memory.
Processor board ID FTX1628AJJH
3 Gigabit Ethernet interfaces
1 terminal line
2 Voice FXO interfaces
4 Voice FXS interfaces
DRAM configuration is 64 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
250880K bytes of ATA System CompactFlash 0 (Read/Write)

Cisco CISCO2921/K9 (revision 1.0) with 483328K/40960K bytes of memory.
Processor board ID FTX1824AKF1
3 Gigabit Ethernet interfaces
1 terminal line
4 Voice FXO interfaces
DRAM configuration is 64 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
250880K bytes of ATA System CompactFlash 0 (Read/Write)

Cisco CISCO2921/K9 (revision 1.0) with 897024K/45056K bytes of memory.
Processor board ID FTX1715ALQL
3 Gigabit Ethernet interfaces
1 terminal line
4 Voice FXO interfaces
4 Voice FXS interfaces
DRAM configuration is 64 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
250880K bytes of ATA System CompactFlash 0 (Read/Write)

Cisco CISCO2921/K9 (revision 1.0) with 483328K/40960K bytes of memory.
Processor board ID FTX1824AKF2
3 Gigabit Ethernet interfaces
1 terminal line
4 Voice FXO interfaces
4 Voice FXS interfaces
DRAM configuration is 64 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
250880K bytes of ATA System CompactFlash 0 (Read/Write)

Cisco CISCO2921/K9 (revision 1.0) with 483328K/40960K bytes of memory.
Processor board ID FTX1822AJ18
3 Gigabit Ethernet interfaces
1 terminal line
4 Voice FXO interfaces
DRAM configuration is 64 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
250880K bytes of ATA System CompactFlash 0 (Read/Write)

Cisco ***CISCO2921 /K9 (revision 1.0) with 471040K/53248K bytes of memory.***
Processor board ID FTX1428AJJF
3 Gigabit Ethernet interfaces
1 terminal line
2 Channelized (E1 or T1)/PRI ports
1 Virtual Private Network (VPN) Module
4 Voice FXO interfaces
4 Voice FXS interfaces
1 Services Module (SM) with Services Ready Engine (SRE)
DRAM configuration is 64 bits wide with parity enabled.
255K bytes of non-volatile configuration memory.
254464K bytes of ATA System CompactFlash 0 (Read/Write)

Fortigate 800D (X2)
(8) GB Memory
S/N--FG800D3916801277
S/N--FG800D3916801012